



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed: December 5, 2003

Examiner: Nguyen, Van Thu T.

Group Art Unit: 2824

Atty. Docket No.: 023-0029

Confirmation No.: 8494

December 7, 2005

Mail Stop Amendment
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37 C.F.R. § 1.97(b)

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- ☒ Form(s) PTO/SB/08A (2 page(s), with copy of 1 reference(s)).
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- ☐ Listing of Pending Applications (N/A page(s), with copy of no application(s)).
- ☐ Other: N/A

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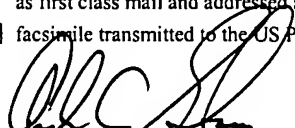
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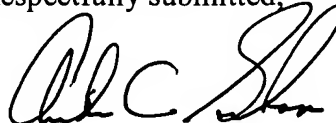
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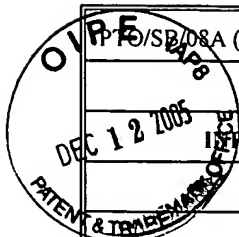
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Respectfully submitted,



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PFO/SB/O&A (Substitute for form 1449/PTO)		Attorney Docket No.: 023-0029
		Application No.: 10/729,865
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: En-Hsing Chen, et al.
		Filing Date: December 5, 2003
		Group Art Unit: 2824
		Examiner Name: Nguyen, Van Thu T.
Sheet 1 of 2		Date Submitted: December 7, 2005

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	AN	WO-2002-43067 US	05-2002			
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